

Attorney's Docket No. 1496.00083

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: David N. Pether et al.  
Serial No.: 09/851,860 Art Unit: 2124  
Filed: May 9, 2001 Examiner: Ngo, C.

For: BIT REDUCTION USING DITHER, ROUNDING AND ERROR FEEDBACK

RECEIVED

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

MAY 20 2004

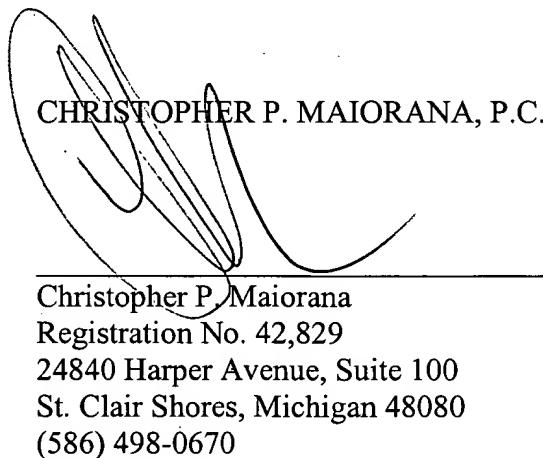
Technology Center 2100

SUBMISSION OF DRAWING AMENDMENT (37 CFR 1.121)

Attached please find

(check applicable items)

- Seven sheets replacement drawings in permanent ink  
 annotated marked-up copy of the amended drawing(s) with annotations showing the change(s).



Dated: May 13, 2004

CERTIFICATE OF MAILING (37 CFR 1.8(a))

I hereby certify that this paper (along with any paper referred to as being attached or enclosed) is being deposited with the United States Postal Service on the date shown below with sufficient postage as first class mail, in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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Jan M. Dunbar

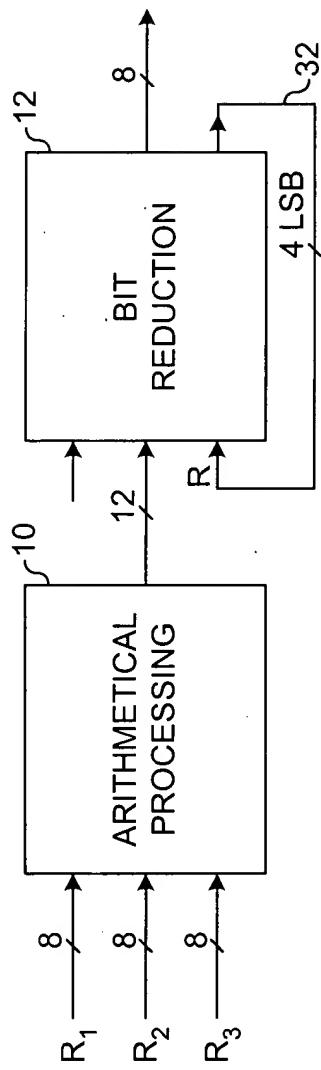
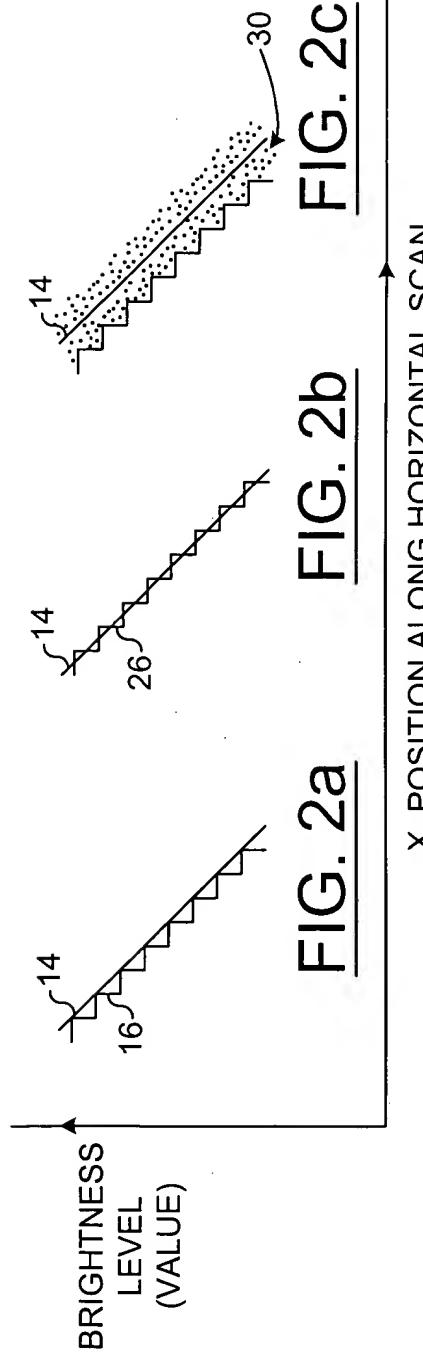


FIG. 1



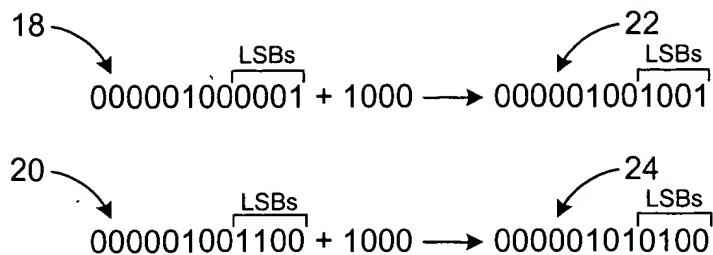


FIG. 3

(X,Y)	0	1	2	3
0	0100	1000	0100	1000
1	1000	1011	1000	1011
2	0100	1000	0100	1000
3	1000	1011	1000	1011

28 → FIG. 4

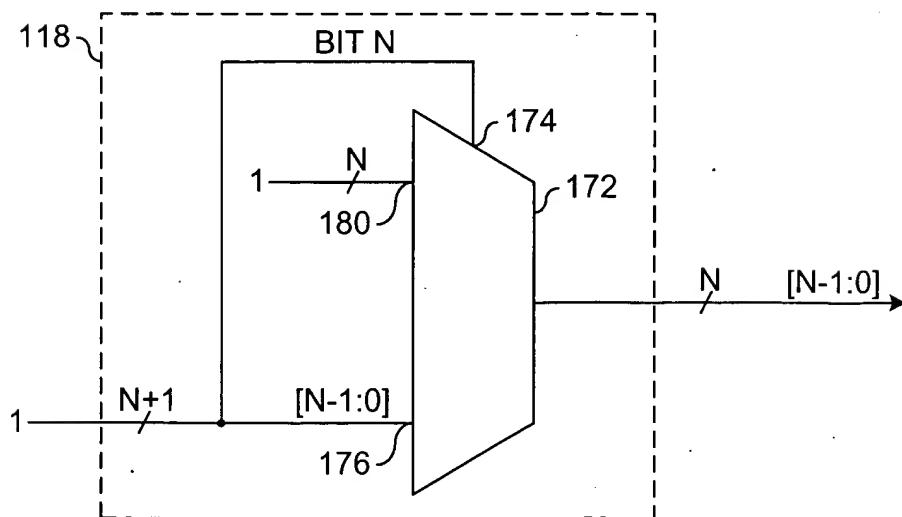


FIG. 6

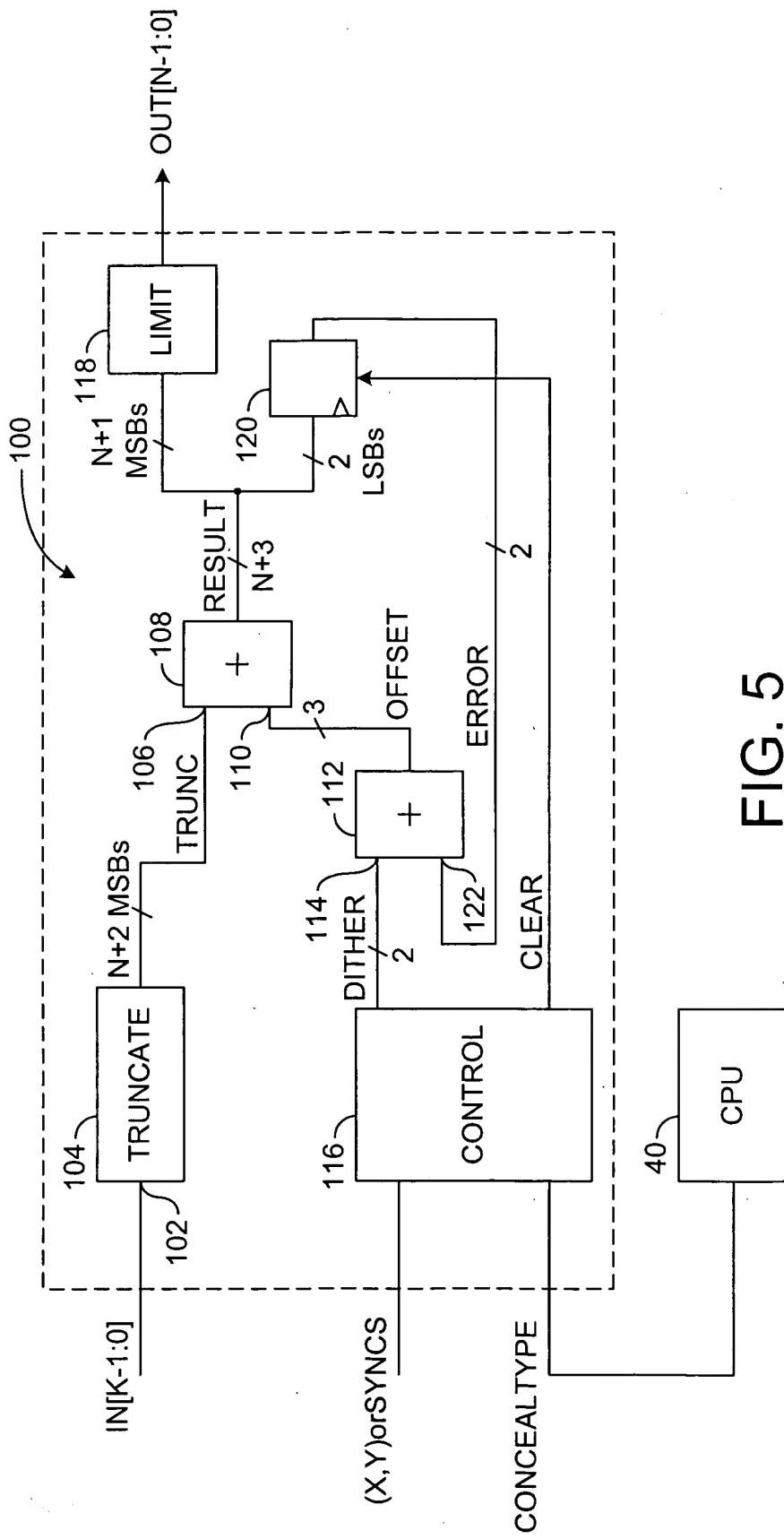


FIG. 5



CONCEALTYPE	Y[0]	X[0]	DITHER[1:0]
TRUNCATE	X	X	00
ROUND	X	X	10
DITHER	0	0	01
	0	1	10
	1	0	10
	1	1	11

FIG. 7

01	10	01	10
10	11	10	11
01	10	01	10
10	11	10	11

FIG. 8

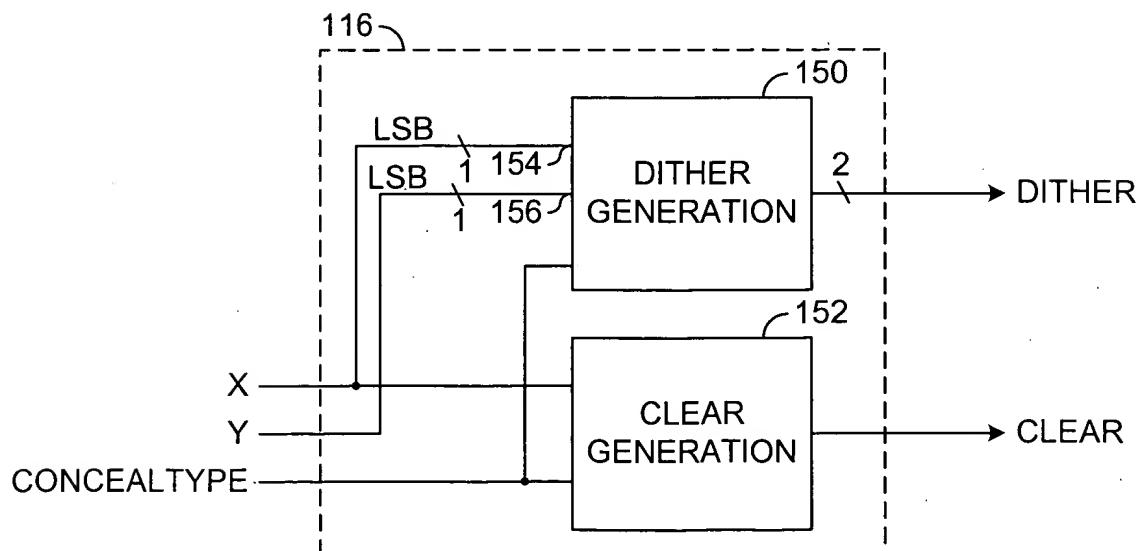


FIG. 9



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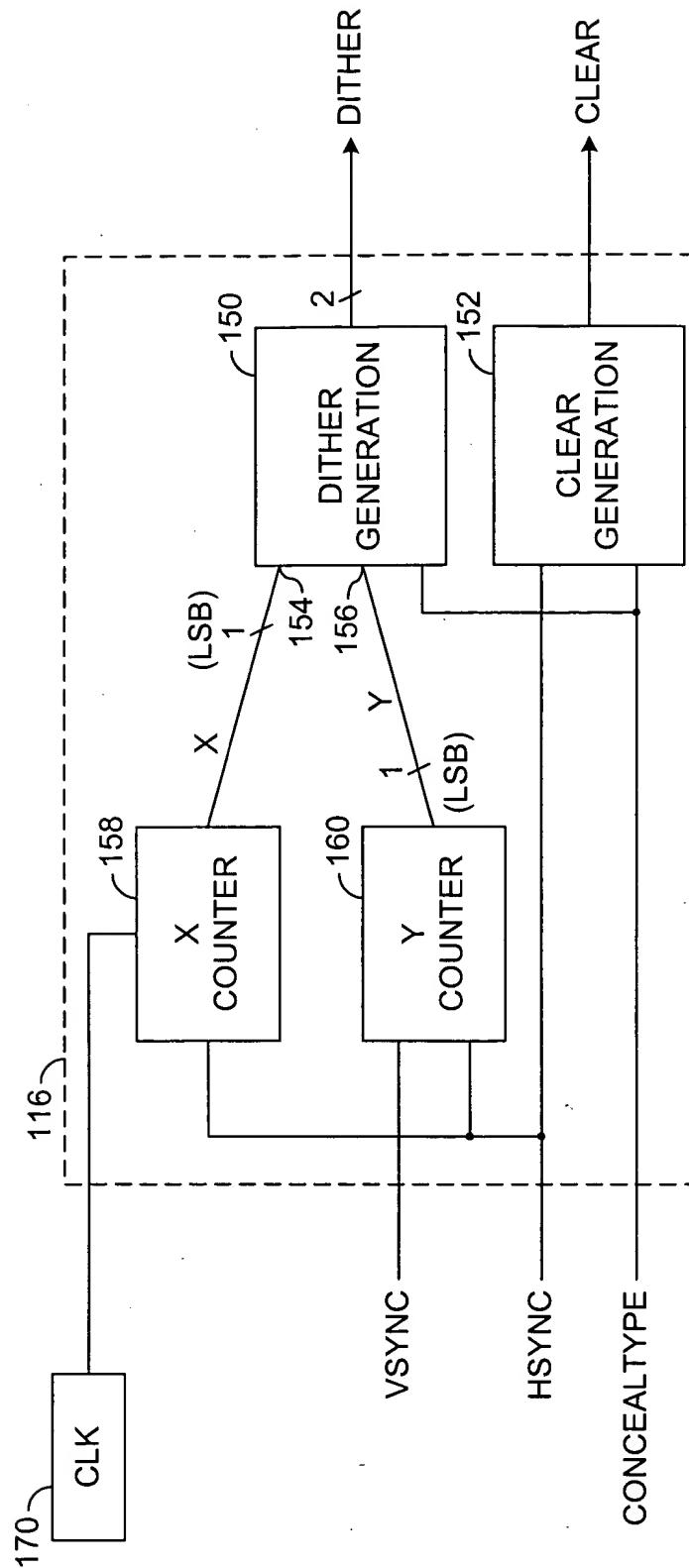


FIG. 10

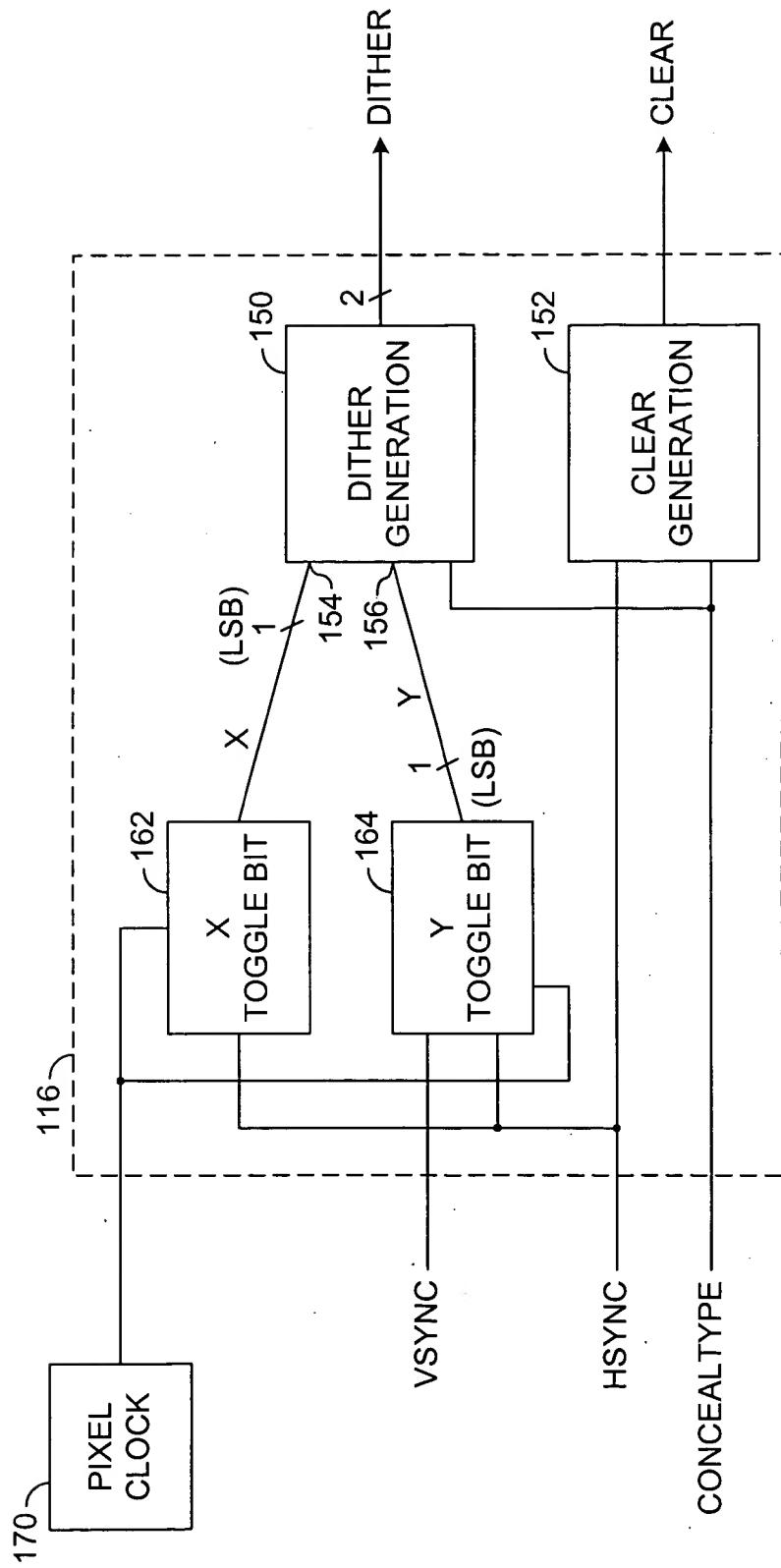


FIG. 11



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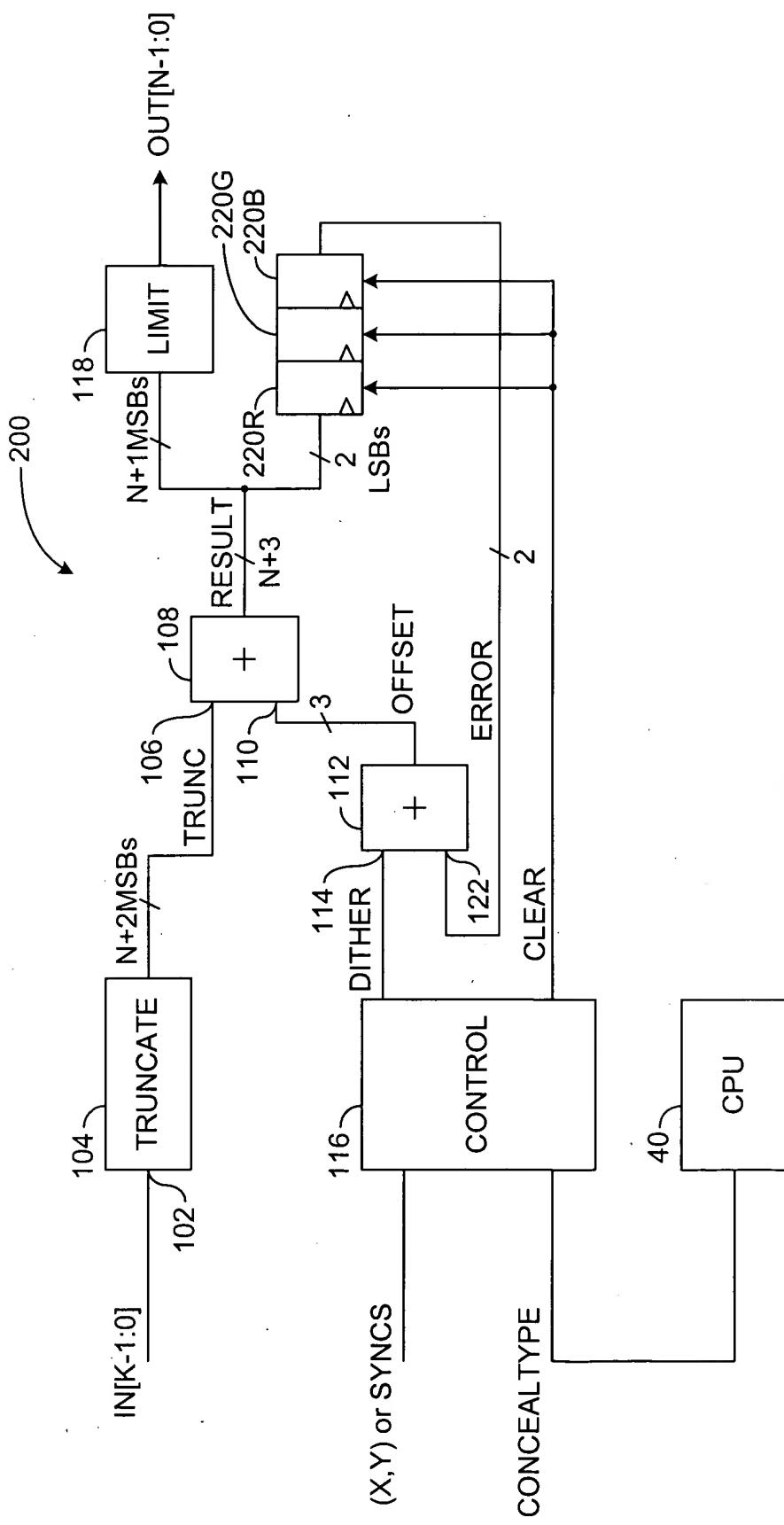


FIG. 12